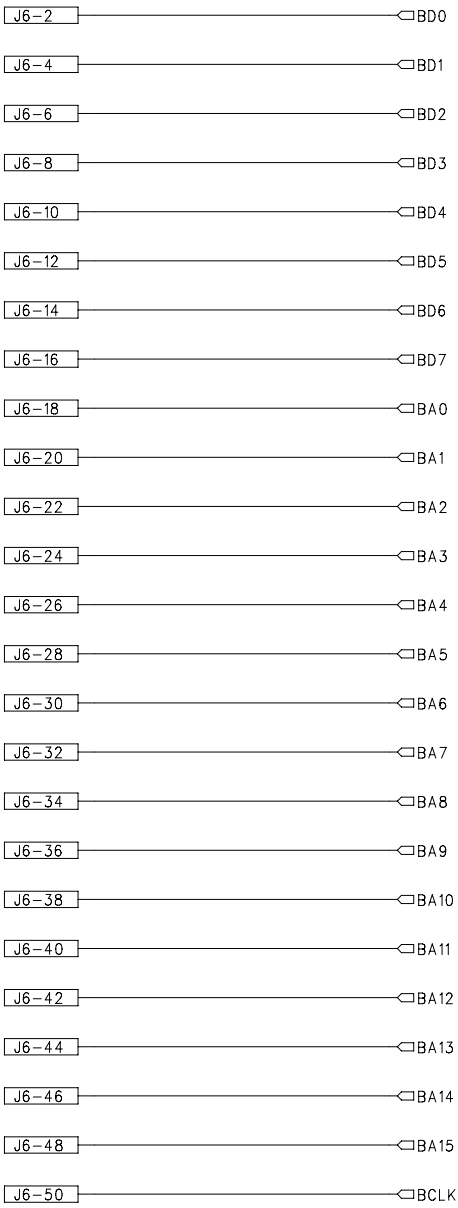
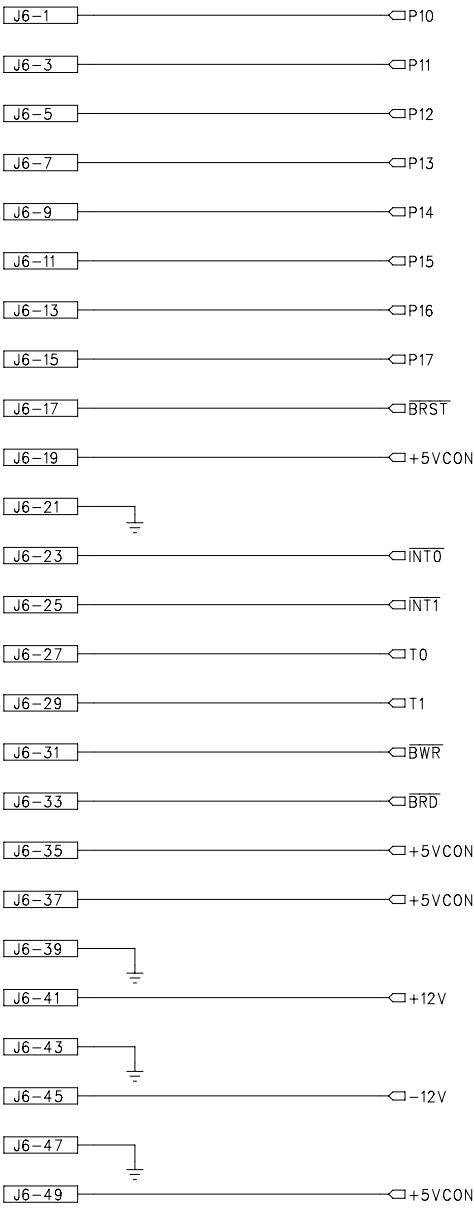
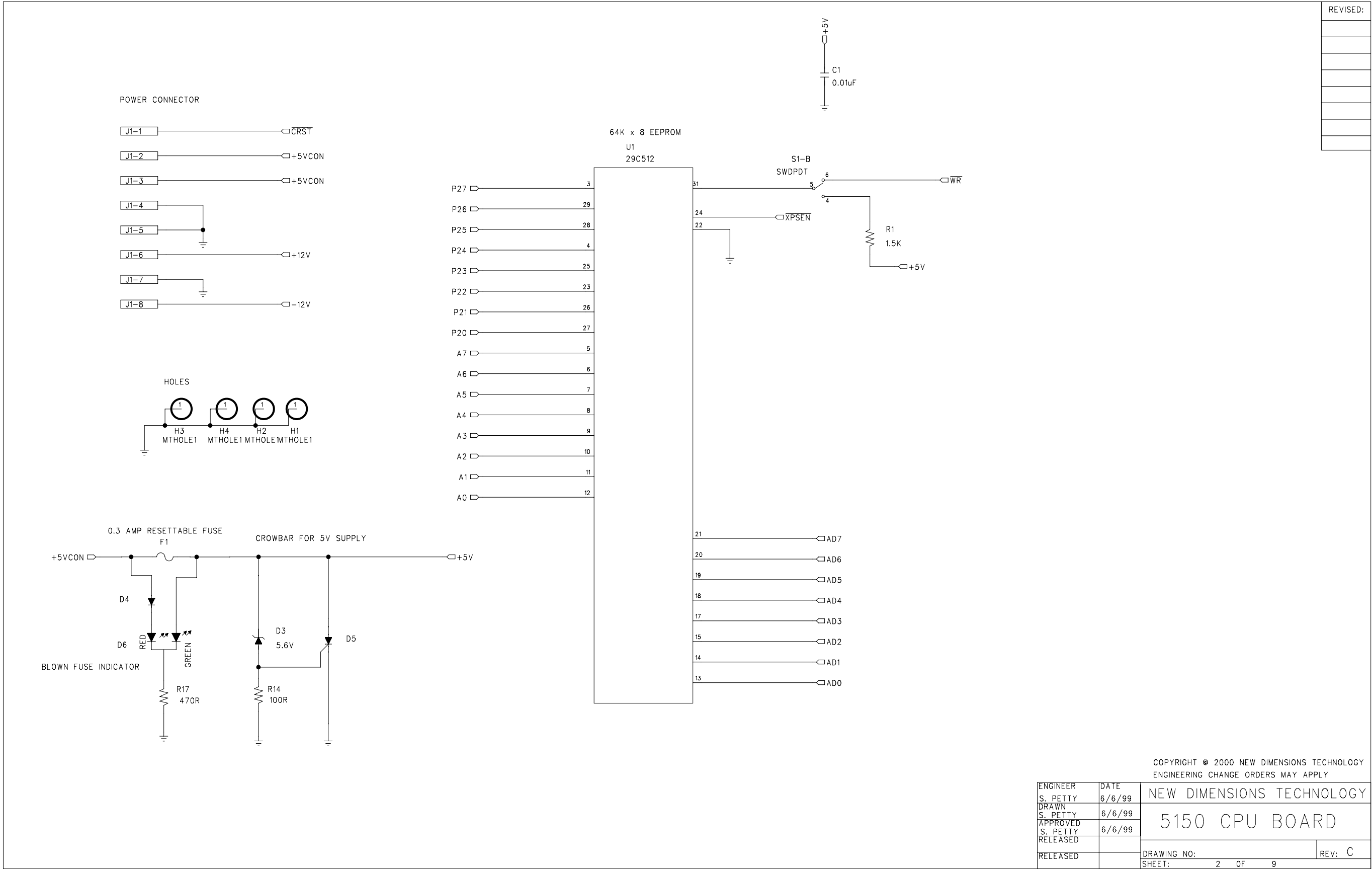


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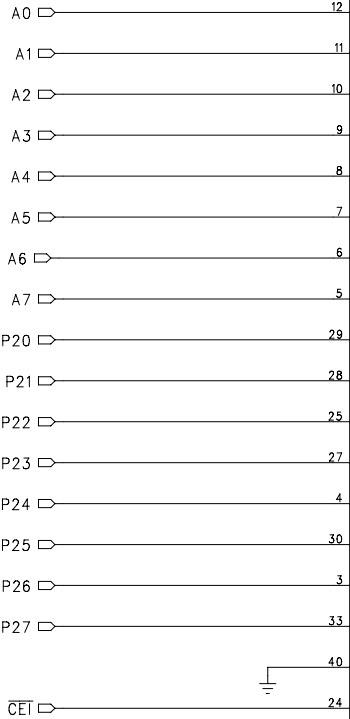
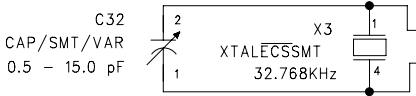


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ENGINEER S. PETTY	DATE 6/6/99	NEW DIMENSIONS TECHNOLOGY	
DRAWN S. PETTY	6/6/99	SCHEMATIC, 5150CPU	
CHECKED S. PETTY	6/6/99	EMBEDDED CONTROLLER CPU	
APPROVED		DRAWING NO: 5150CPU	REV: C
RELEASED		SHEET: 1 OF 9	FILENAME: 5150CPUC.SCH

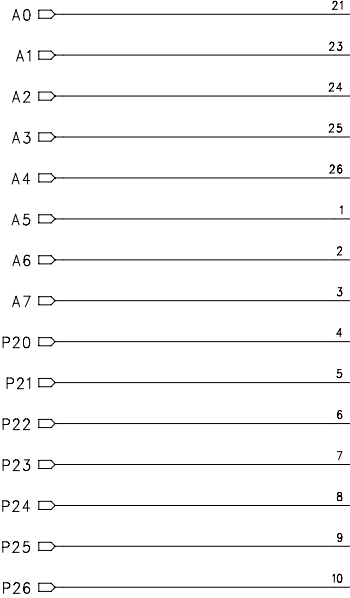
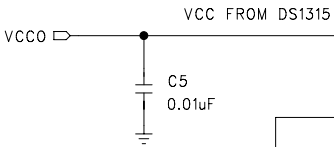
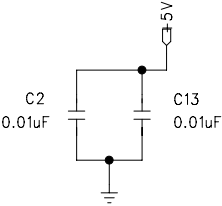
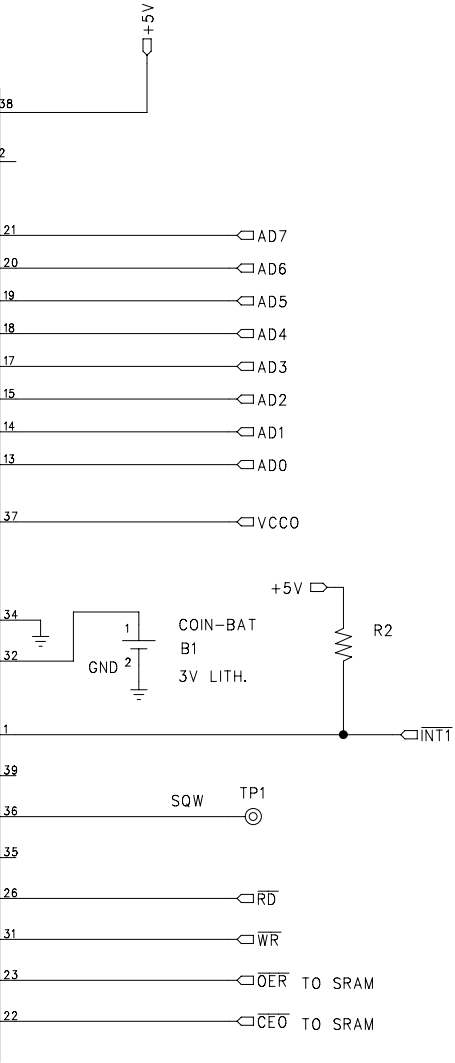


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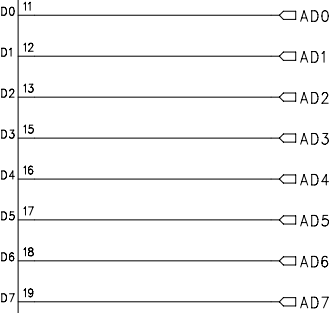
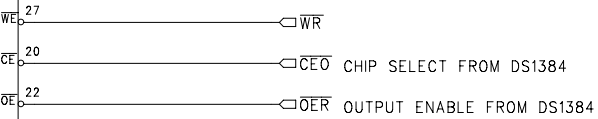
ADJUST UNTIL 1024.00 Hz OUTPUT
AT SQW TEST POINT



U2
DS1384



U4
62256A/TSOP28

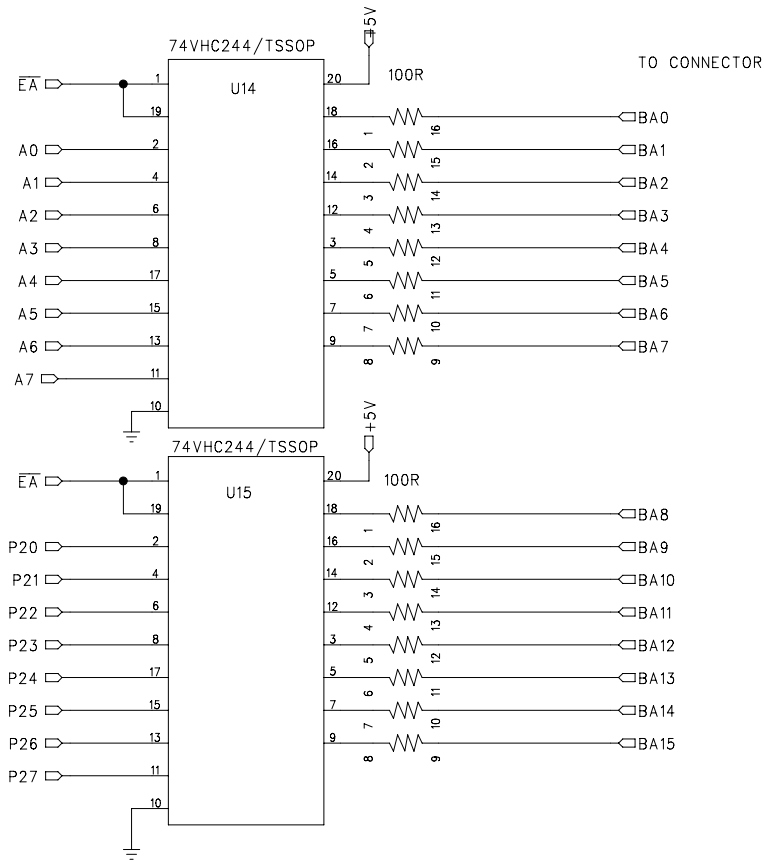
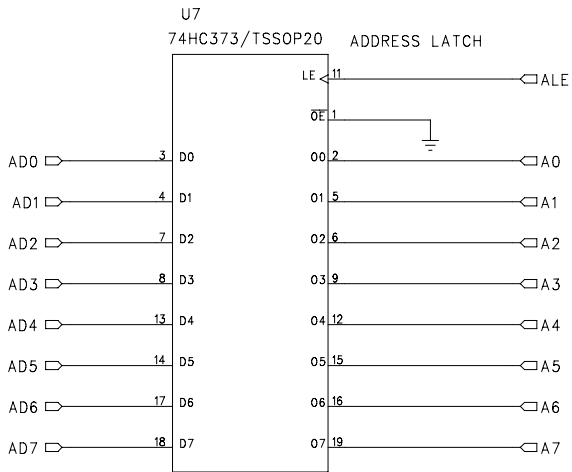
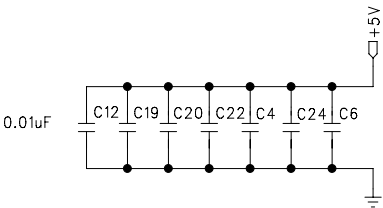
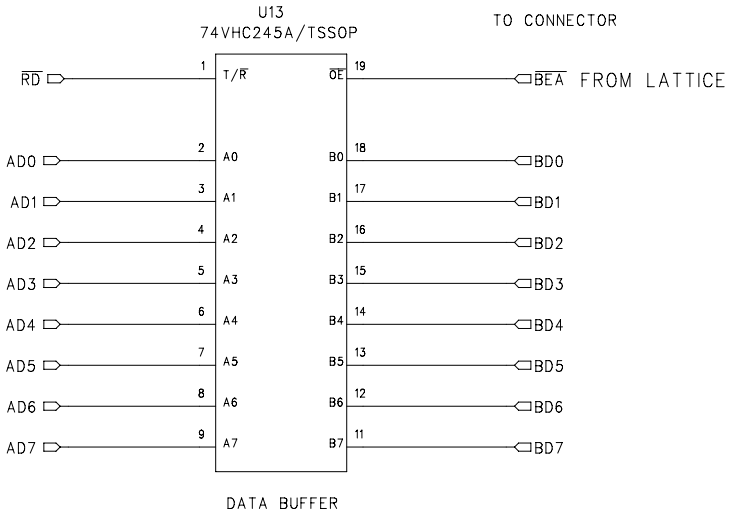
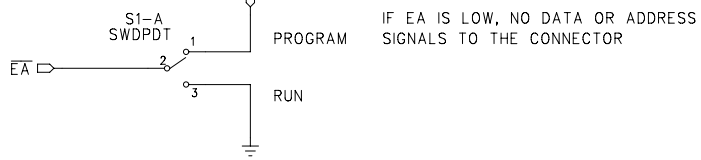
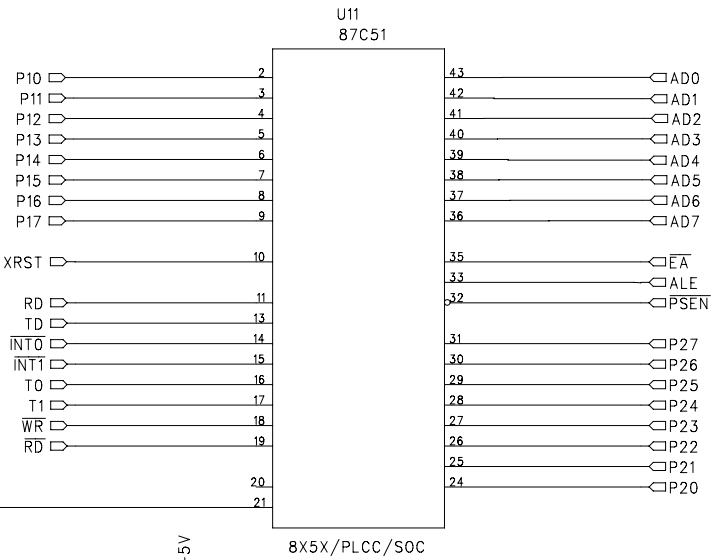
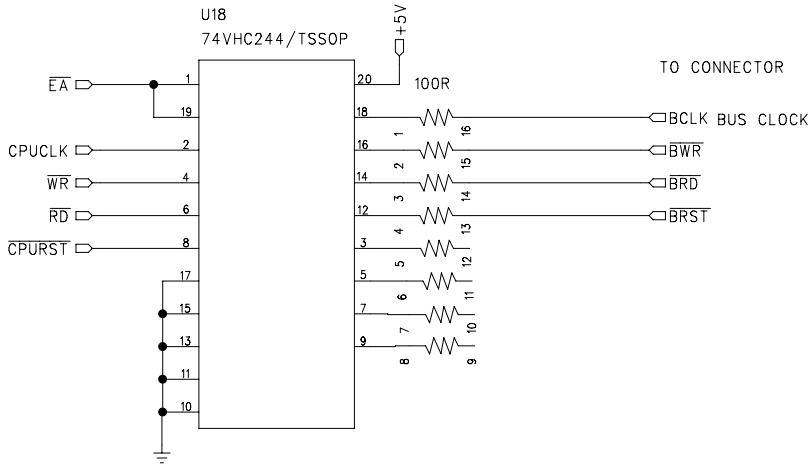
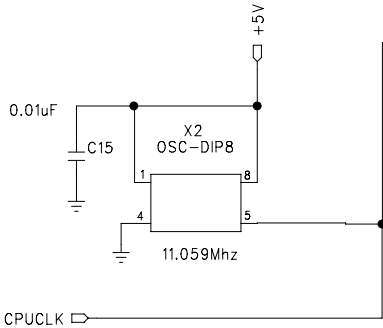
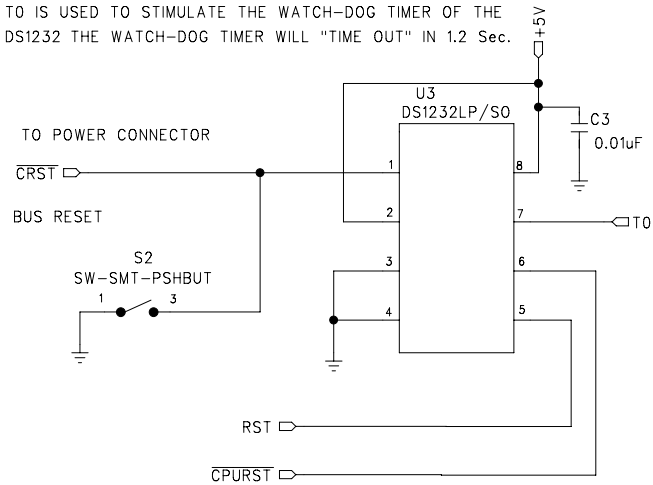


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ENGINEERING CHANGE ORDERS MAY APPLY

ENGINEER S. PETTY	DATE 6/6/99	NEW DIMENSIONS TECHNOLOGY	
DRAWN S. PETTY	6/6/98	5150 CPU BOARD	
CHECKED S. PETTY	6/6/98		
APPROVED		DRAWING NO:	REV: C
RELEASED		SHEET: 3 OF 9	

REVISED:

T0 IS USED TO STIMULATE THE WATCH-DOG TIMER OF THE DS1232 THE WATCH-DOG TIMER WILL "TIME OUT" IN 1.2 Sec.

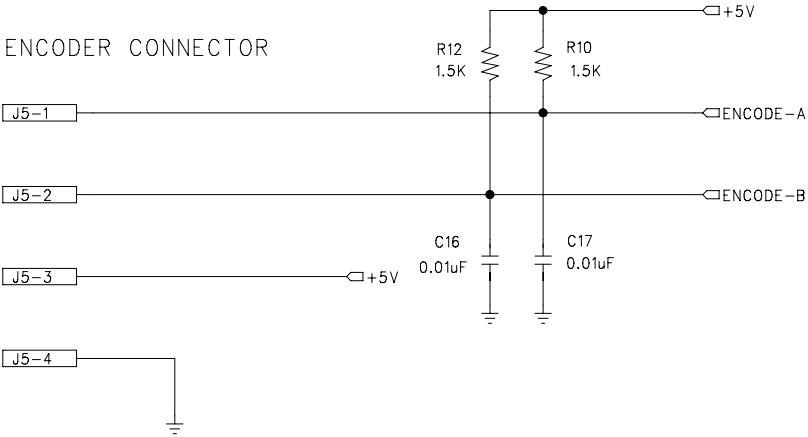


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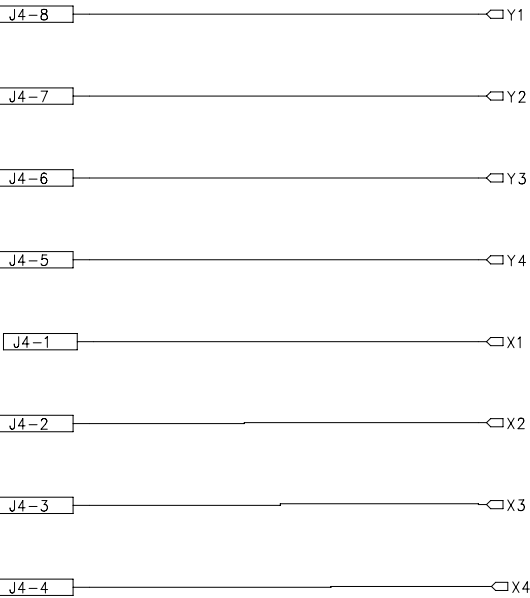
ENGINEER S. PETTY	DATE 6/6/99	NEW DIMENSIONS TECHNOLOGY	
DRAWN S. PETTY	6/6/99	5150 CPU BOARD	
CHECKED S. PETTY	6/6/99		
APPROVED		DRAWING NO:	REV: C
RELEASED		SHEET:	4 OF 9

REVISED:

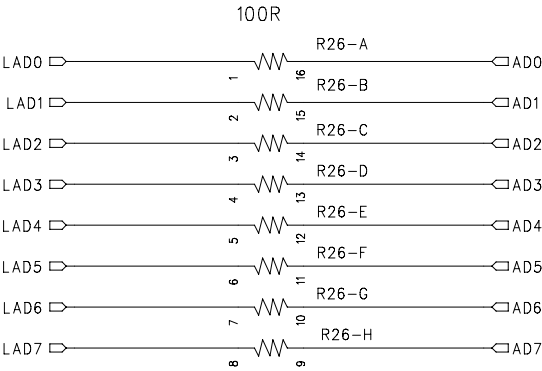
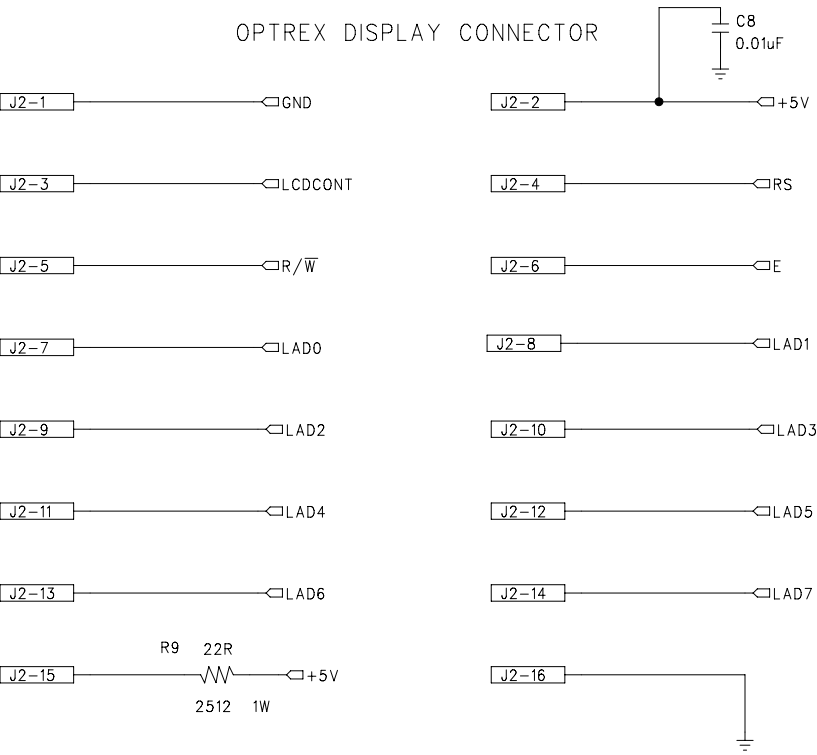
ENCODER CONNECTOR



KEYPAD CONNECTOR



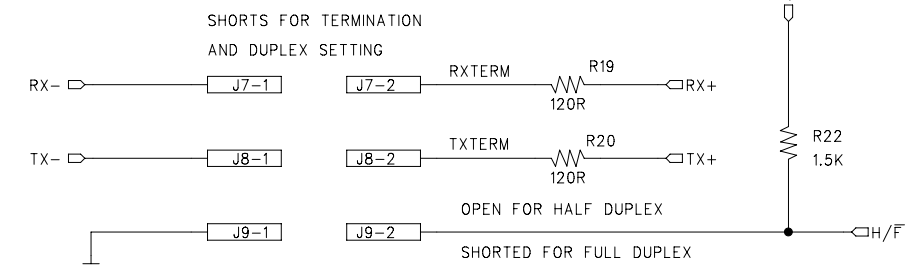
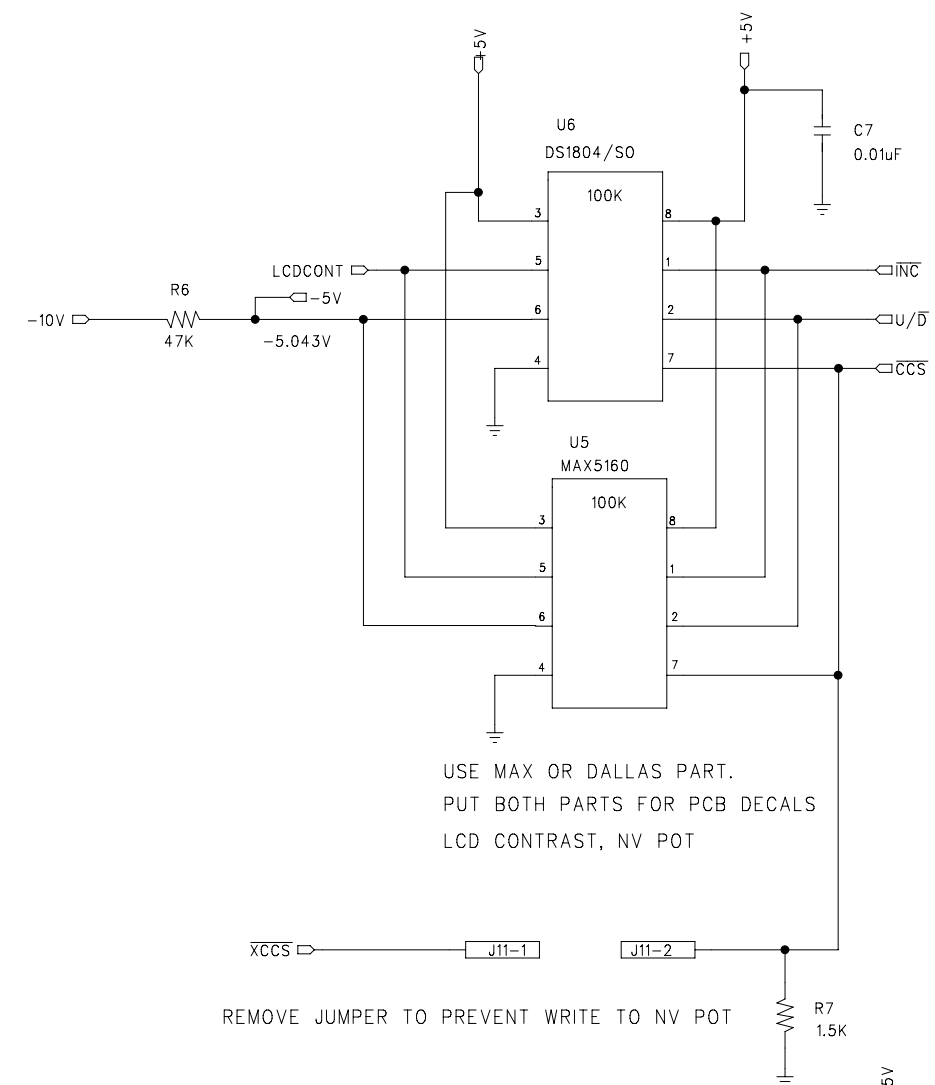
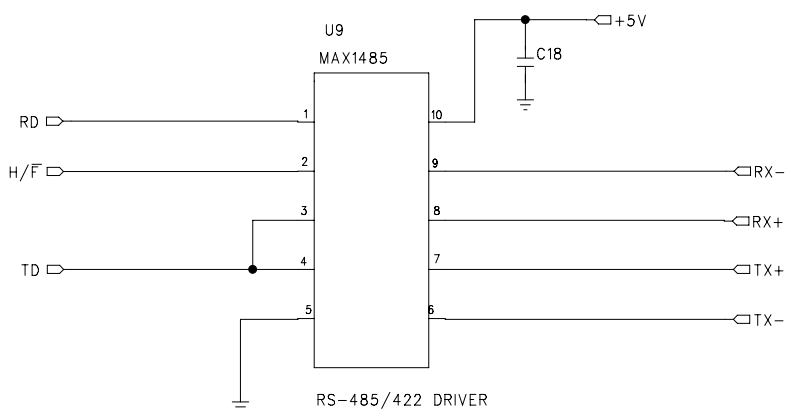
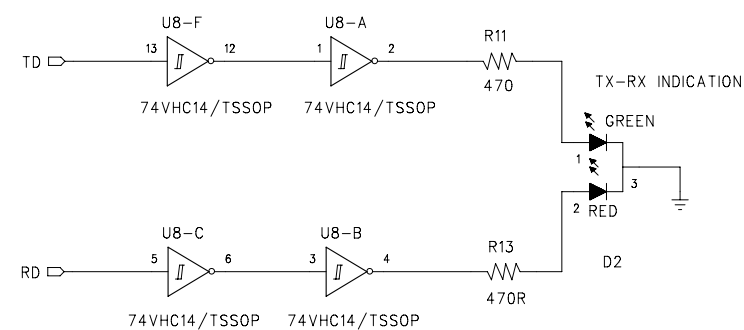
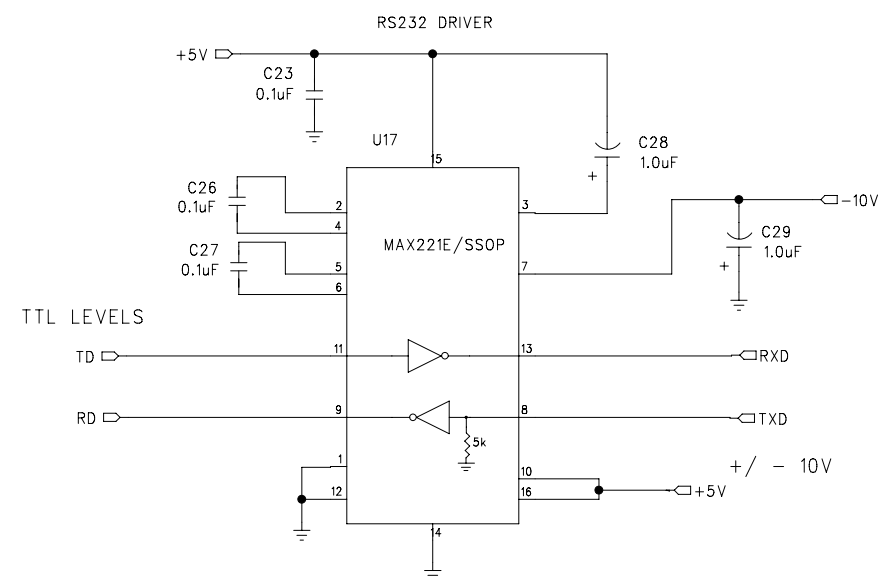
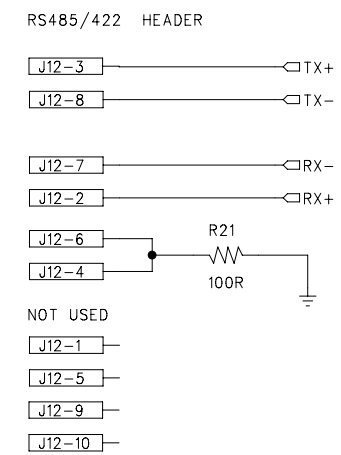
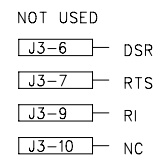
OPTREX DISPLAY CONNECTOR



SOURCE TERMINATION FOR LONG DISPLAY CABLES

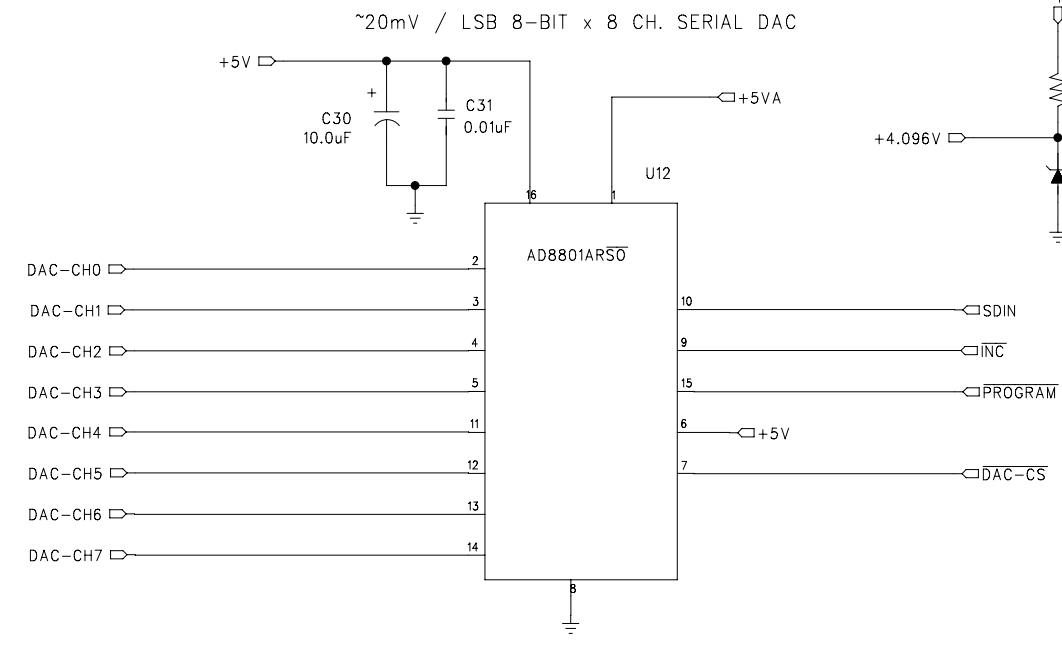
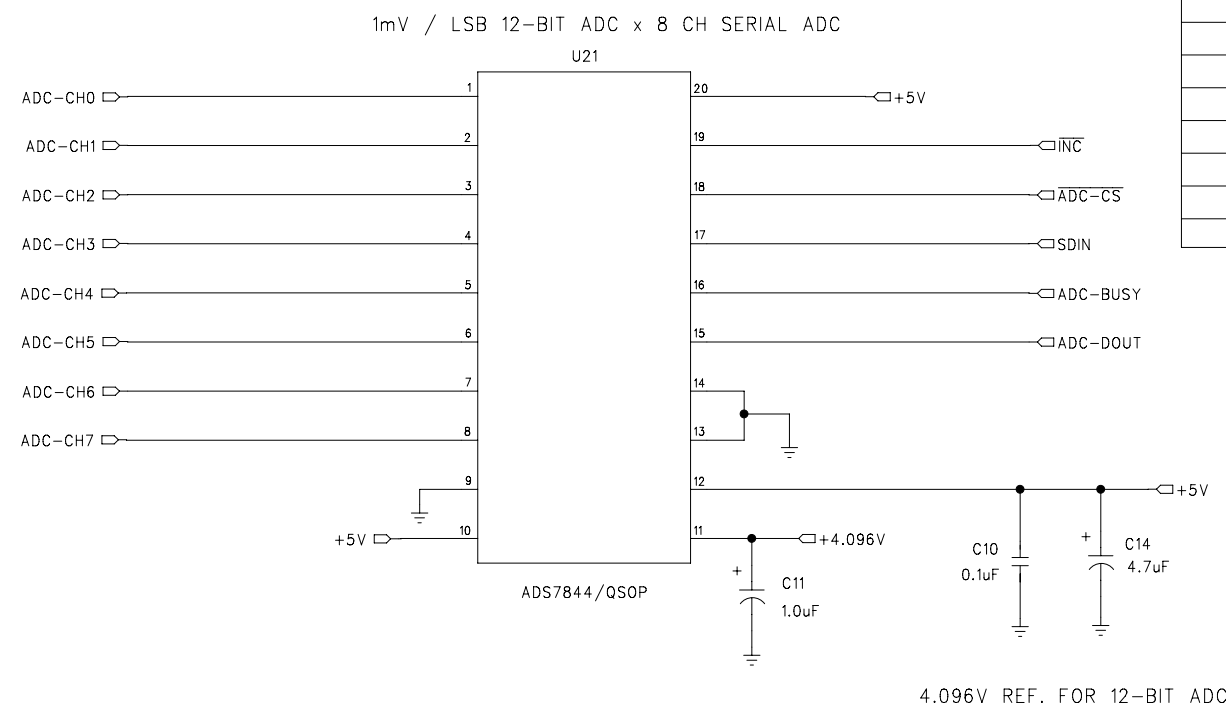
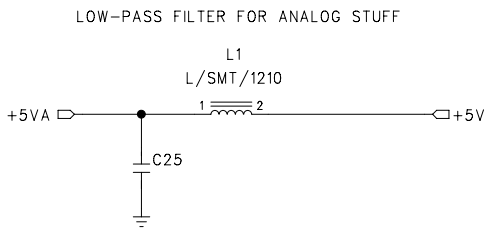
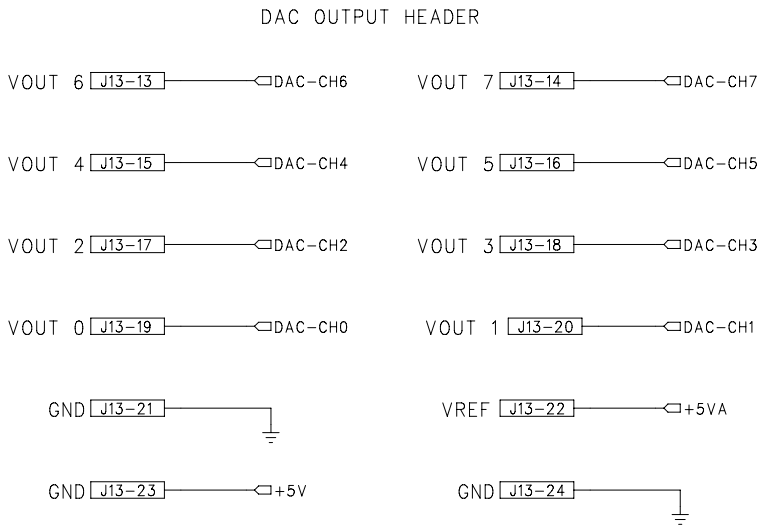
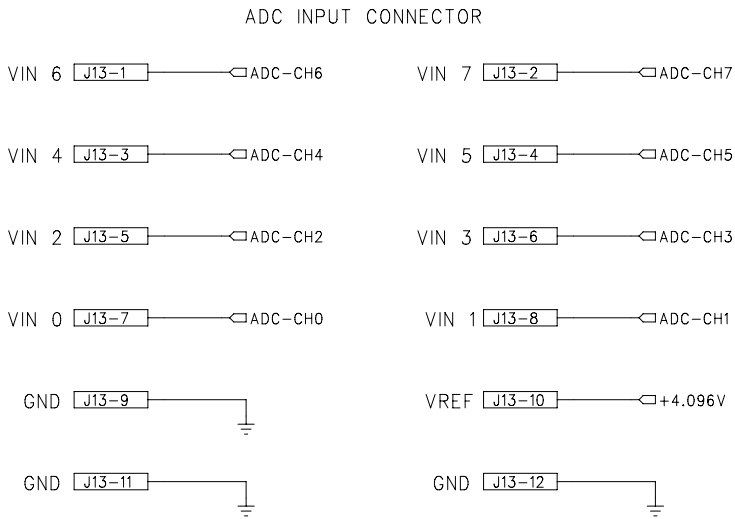
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ENGINEER	DATE	NEW DIMENSIONS TECHNOLOGY	
S. PETTY	6/6/99		
DRAWN	6/6/99	5150 CPU BOARD	
S. PETTY	6/6/99		
CHECKED	6/6/99		
S. PETTY	6/6/99		
APPROVED			
RELEASED		DRAWING NO:	REV: C
		SHEET: 6 OF 9	



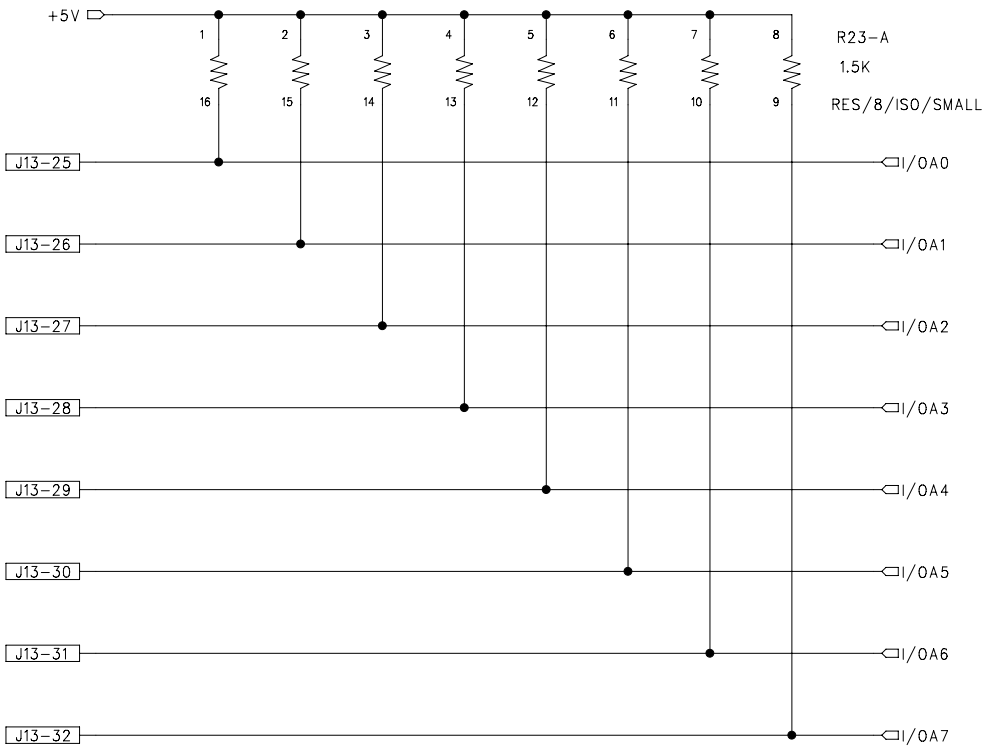
ENGINEER S.PETTY		DATE 4/29/00	COPYRIGHT © 2000 NEW DIMENSION TECHNOLOGY ENGINEERING CHANGE ORDERS MAY APPLY	
DRAWN S.PETTY		4/29/00	NEW DIMENSIONS TECHNOLOGY	
APPROVED			SERIAL DRIVERS & INDICATION LCD CONTRAST NV POT.	
RELEASED				
RELEASED			DRAWING NO: _____	
			SHEET: 7 OF 9	
			REV: C	

[illegible]

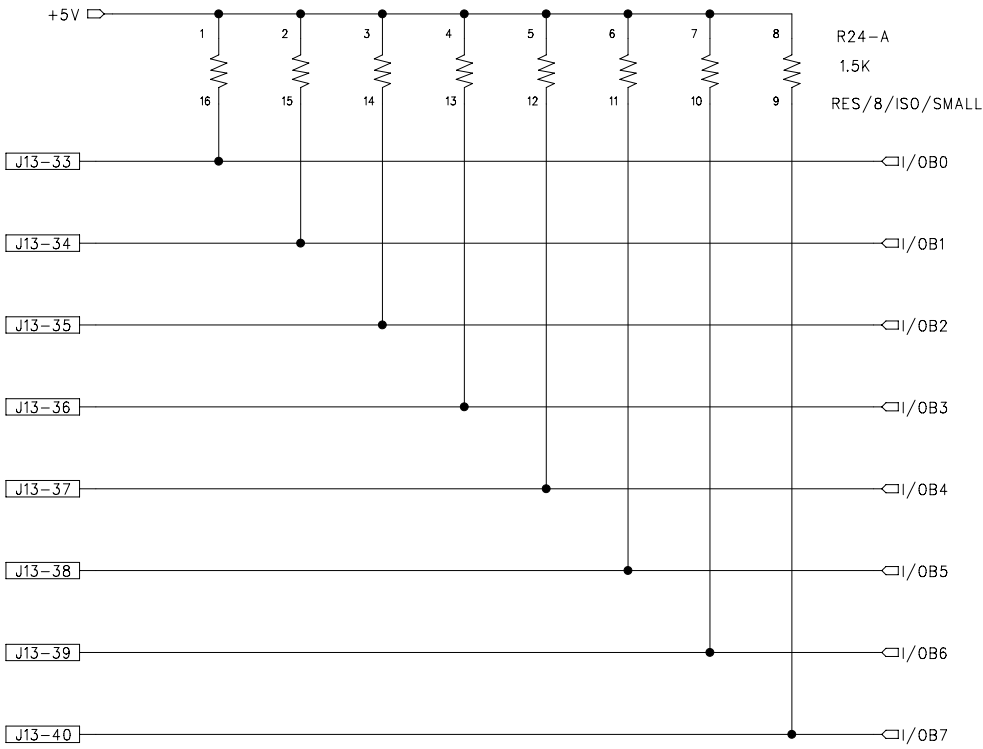


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ENGINEER S.PETTY	DATE 4/30/00	NEW DIMENSIONS TECHNOLOGY	
DRAWN S.PETTY	4/30/00	8-BIT DAC 8 CHANNELS	
APPROVED		12-BIT DAC 8 CHANNELS/ 4 DIF.	
RELEASED			
RELEASED		DRAWING NO:	REV: C
		SHEET: 8 OF 9	

PORT A CONNECTOR



PORT B CONNECTOR



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ENGINEER S.PETTY	DATE 4/29/00	NEW DIMENSIONS TECHNOLOGY	
DRAWN S.PETTY	4/29/00	DIGITAL I/O CONNECTOR	
APPROVED			
RELEASED			
RELEASED		DRAWING NO:	REV: C
		SHEET: 9 OF 9	